Grad student receives major research scholarship

DENTON (UNT), Texas -- Rahul Allawadhi, a graduate student in engineering at the University of North Texas, recently received a $24,000 graduate scholarship for his studies in electronic design automation/circuit design. The scholarship is sponsored by the Design Automation Conference -- a premier electronic design automation and silicon solution event.

DAC will support Allawadhi in his work on a project titled SPICE Modernization. SPICE is an acronym for Simulation Program with Integrated Circuit Emphasis.

"SPICE has been the main tool used in the design of integrated circuits since its original development at the University of California, Berkeley in the 70's and 80's," said Elias Kougianos, assistant professor in the Department of Engineering Technology. "Today, practically every integrated circuit design center uses SPICE or one of its commercial variants during the integrated circuit development process."

Allawadi said he is excited and eager to work on the project under the guidance of his professors Kougianos and Saraju Mohanty, assistant professor of computer sciences and engineering, as well as with his colleagues in the design laboratory.

Oscar Garcia, founding dean of the College of Engineering, said the award is a national honor from a very well-established national conference.

"I would like to congratulate our engineering technology faculty member, Elias Kougianos and his student Rahul Allawadhi," he said.

Kougianos said the one-year scholarship awarded to UNT will allow him and Allawadhi to modify and enhance the original Berkeley code and make it more appropriate for today's high-speed, high-performance integrated circuit designs.

"This effort is the starting point for the development of a robust and nationally known research program in the wider area of Electronic Design Automation," he said.

"This recent award shows that UNT is well on its way towards recognition in this key technology area with the recent creation of the very large integrated circuit design & computer aided design laboratory," Kougianos said. "The laboratory is already attracting national attention because of the high quality of research undertaken by its principal members and graduate students."

Co-founded by Kougianos and Mohanty, information about the laboratory may be found at [www.vdcl.cse.unt.edu](http://www.vdcl.cse.unt.edu).

The scholarship winners were evaluated on the basis of quality and applicability of the proposed research, impact of the award on the program at the supervising faculty's institution, academic credentials of students and financial need.

A formal presentation of the award will take place at 8:30 a.m. June 14 (Tuesday) at the 42nd Design Automation Conference in Anaheim, Calif.

DAC will feature more than 50 technical sessions covering the latest in design methods and electrical design automation tool developments. More than 250 electrical design and
automation silicon and intellectual property providers will participate in this year's conference.

Approximately 10,500 plus attendees will participate in this annual conference, scheduled for June 13 to 17.

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