

Lab 3 (30 Sep 2004): CSCI 4330 /5330: Digital Systems Design with VHDL
Instructor: Saraju P. Mohanty

Write complete VHDL code of the first four examples discussed in 21 Sep 04 lecture and simulate them using CAD tool. Submit a report that includes design VHDL codes, test bench VHDL code, and simulation waveforms.