

Lab 9 (11 Nov 2004) CSCI 4330/5330: Digital Systems Design with VHDL  
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NOTE: Show all logic minimization process/steps in your report.

1. 2.46 (text book page-71)
2. 2.47 (text book page-72)
3. 4.39 (text book page-241)
4. 4.40 (text book page-242)
5. Write a while loop that calculates the exponential function  $x$  to an accuracy of one part in  $10^4$  by summing terms of the following series:  
$$e^x = 1 + x/1 + x^2/2! + x^3/3! + x^4/4! + \dots$$