

Test 3 Syllabus
CSCI 5330: Digital CMOS VLSI Design
Instructor: Dr. Saraju P. Mohanty

NOTE:

- This is closed book, closed text examination.
- Calculators are NOT allowed in the examination.

NOTE: All material covered from 24th Apr 05 onwards, all the term papers and projects under taken by students is the syllabus of the Test 3.

1. Pseudo-nMOS Logic
2. Dynamic Logic
3. Pass Transistor Logic
4. Sequencing
5. Sequencing Element Design
6. Max and Min-Delay
7. Clock Skew
8. Time Borrowing
9. Two-Phase Clocking
10. Single-bit Addition
11. Carry-Ripple Adder
12. Carry-Skip Adder
13. Carry-Look ahead Adder
14. Carry-Select Adder
15. Carry-Increment Adder
16. Tree Adder
17. All term papers
18. All projects