

CSCE 5730: Digital CMOS VLSI Design

Instructor: Dr. Saraju P. Mohanty

Assignment 2

Assigned Dated: 06th Feb 2006 (Mon)

Due Date: 13th February 2006 (Mon)

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1. Design and verify the basic logic gates, such as NAND, NOR, AND, OR, Inverter using DSCH tool. Provide printouts for the schematic designs and simulation results.